

8 Series P5 Processors

Model Numbers	H8P5-120: Wired Processor only. H8P5-D48-120: Wired Processor with one integral Dimmer Interface (D48). H8P5-H48-120: Wired Processor with one integral Dimmer Interface (H48). H8P5-MI-120: Wired Processor with one integral Module Interface. H8P5-MI-D48-120: Wired Processor with one integral Module Interface and one integral Dimmer Interface (D48). H8P5-MI-H48-120: Wired Processor with one integral Module Interface and one integral Dimmer Interface (H48).
Input Voltage	120 V \sim 50/60 Hz
Regulatory Approvals	UL, CSA, NOM
Environment	Ambient operating temperature: 0 °C to 40 °C, 32 °F to 104 °F Ambient operating humidity: 0-90% humidity, non-condensing. Indoor use only.
Cooling Method	Passive cooling.
Heat Generated Fully Loaded	18 BTUs per hr.
Line-Voltage Connections	Mates with Lutron-provided 2-pin pigtail on DIN-rail terminal block. Power switch provided on top left of processor. Terminal blocks should be tightened to 3.5-5.0 in.-lbs. (0.40-0.57 N•m)
Low-Voltage Wire Type	Two pair — one pair #18 AWG (1.0 mm ²), one pair #18-22 AWG (1.0-0.5 mm ²) twisted shielded — NEC® Class 2 (IEC PELV) cable.
Low-Voltage Wiring Configuration	All processors in a multi-processor system must have the inter-processor communication links connected in a daisy-chain configuration.
Low-Voltage Connections	4-pin removable terminal block. Each of the four terminals will accept up to two #18 AWG (1.0 mm ²) wires. Up to two standard female DB-9 serial RS-232 connections and one RJ-45 standard ethernet connection.
Addressing	Via DIP Switch. Counts as 1 of 16 processor addresses. <i>See Fig. 7, pg. 96.</i>
Diagnostics	Power LED, Communication link power short circuit LED, Links 1-8 Tx and Rx LEDs.
ESD Protection	Meets or exceeds the IEC 61000-4-2 standard.
Surge Protection	Meets or exceeds ANSI/IEEE standard c62.41.
Miswire Protection	All terminal block inputs are over-voltage and miswire protected against wire reversals and shorts.
Power-Failure Memory	Lithium battery provides a minimum of ten years of data retention.
Internal Timeclock	Accuracy \pm 1 minute per year (specified as during data retention time).
Mounting	HWI-PNL-8: Processor mounts at bottom of panel. <i>See Fig. 10, pg. 98.</i> HWI-LV32-120: Processor mounts at top of enclosure. <i>See Fig. 9, pg. 98.</i>
Mounting Hole Locations	<i>See Fig. 6, pg. 96.</i>
Shipping Weight (all model numbers)	9 lbs. (4.1 kg)

8 Series P5 Processors (cont.)

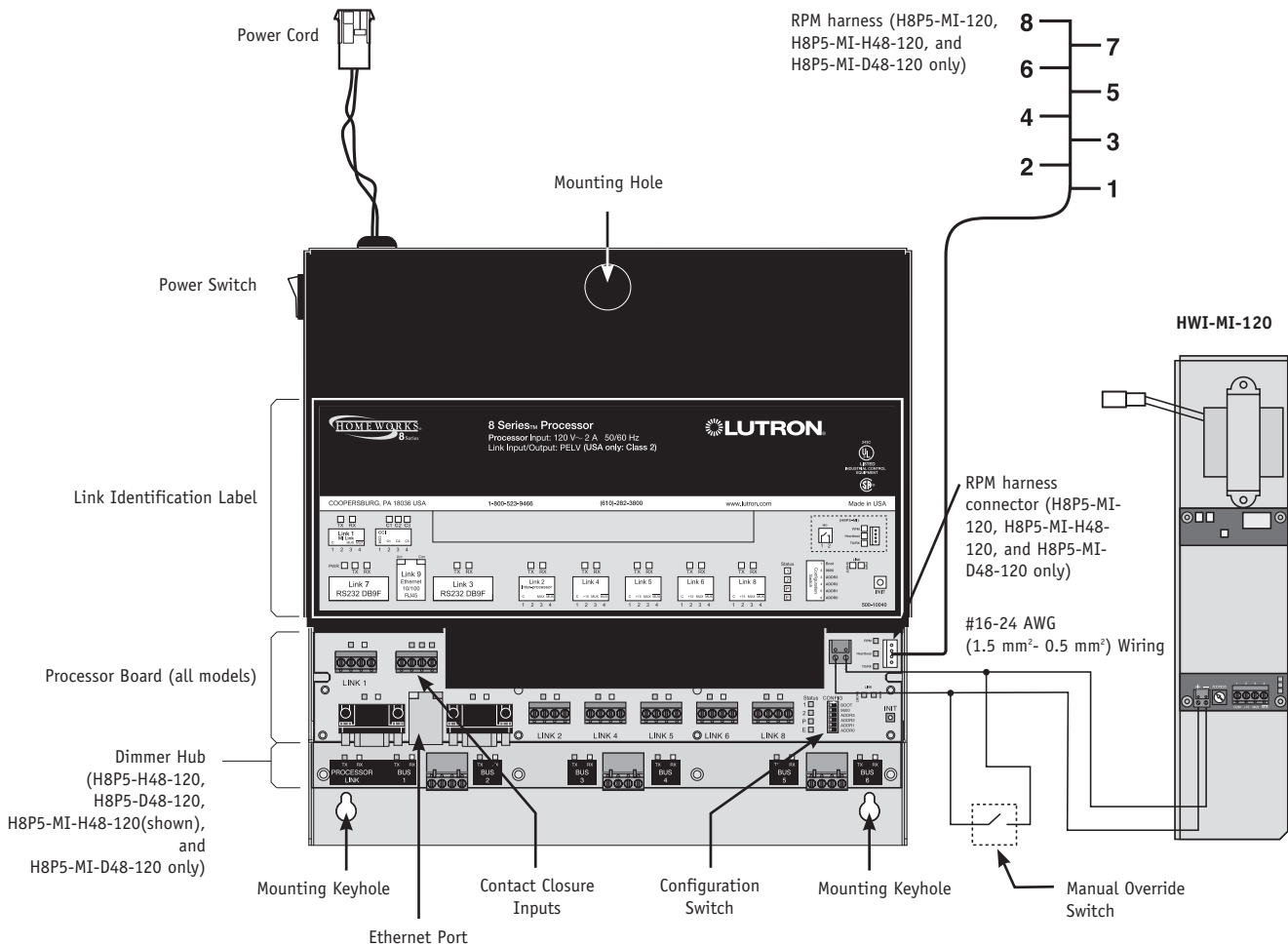


Figure 6 - 8 Series P5 Processor (H8P5-MI-H48-120 shown)

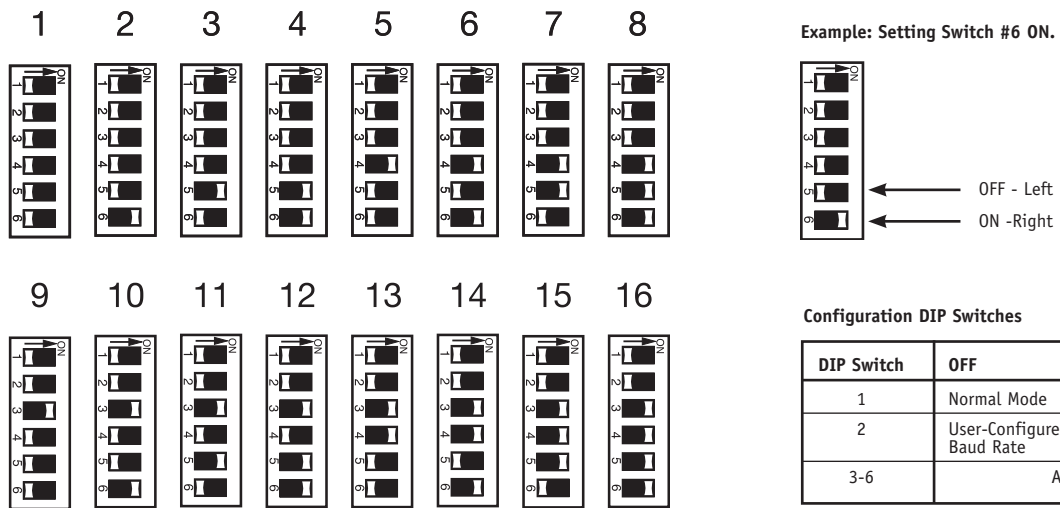


Figure 7 - Address DIP Switch Settings (configure switch S1)

BACK ROOM

8 Series P5 Processors (cont.)

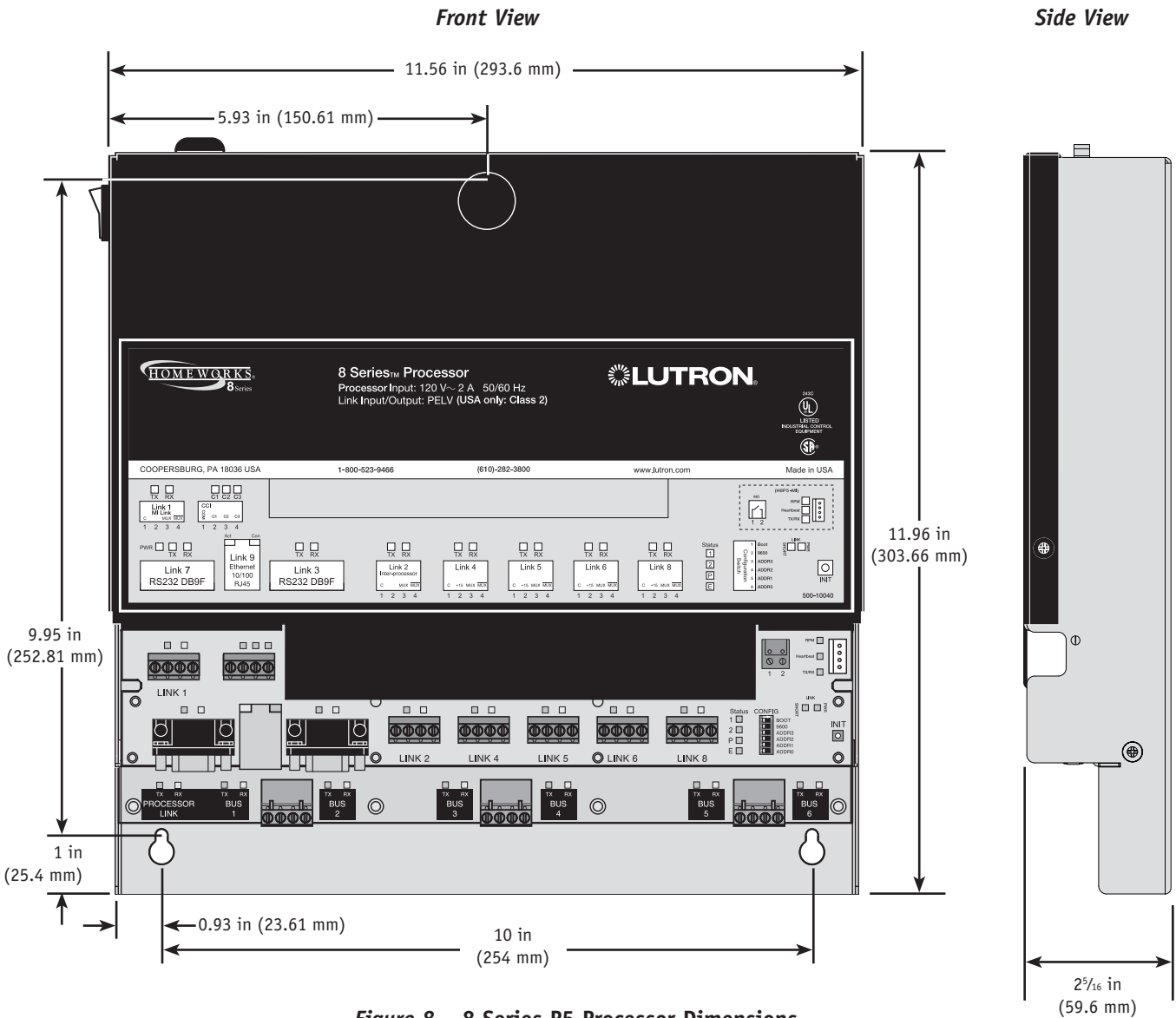


Figure 8 – 8 Series P5 Processor Dimensions

BACK ROOM

8 Series P5 Processors (cont.)

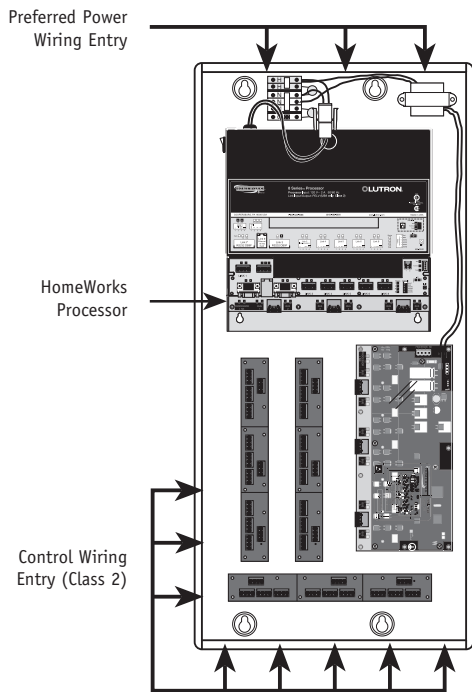


Figure 9 – Mounting Location in an HWI-LV32-120

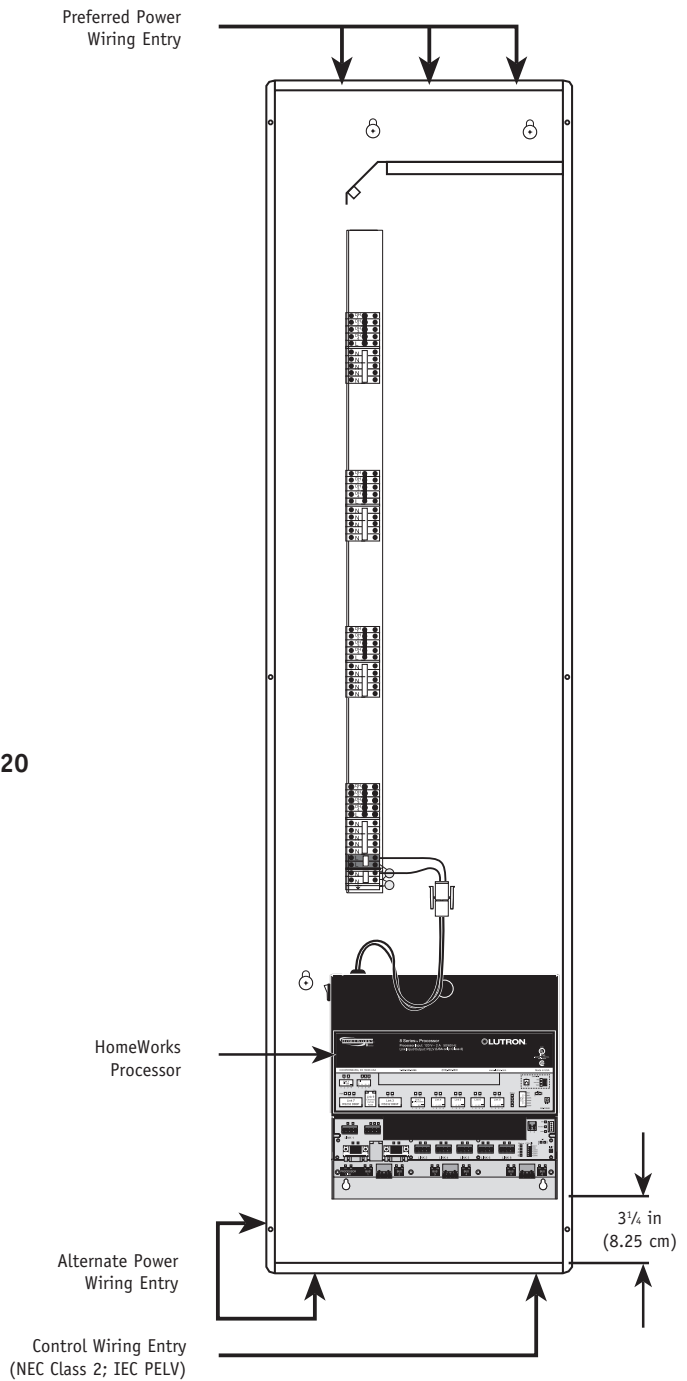


Figure 10 – Mounting Location in an HWI-PNL-8

BACK ROOM